

1. A compiler device to generate a code for performing a procedure call in a program, making use of a dynamically and thread-piece-wise allocated interface area when a plurality of threads are processed in parallel.

code converting means for converting a reference to the interface area in a source program into a code for performing a reference making use of the leading address determined by executing said code generated by said code generating means.

4. A compiler device of claim 2,  
wherein said code generating means is constituted to generate a code for determining the leading address of the interface area, based on a designation by a user.

5. A computer-readable recording medium recorded with a compiler program for causing a computer to realize a function to generate a code for performing a procedure call in a program, making use of a dynamically and thread-piece-wise allocated interface area when a plurality of threads are processed in parallel.

6. A computer-readable recording medium recorded with a compiler program for causing a computer to realize:  
a code generating function for generating a code for determining a leading address of a dynamically and thread-piece-wise allocated

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interface area, and

a code converting function for converting: a reference to the interface area in a source program into a code for performing a reference making use of the leading address determined by executing said code generated by said code generating function.

7. A computer-readable recording medium recorded with a compiler program of claim 6,

wherein said code generating function is constituted to generate a code for calling a library for determining the leading address of the dynamically and thread-piece-wise allocated interface area.

8. A computer-readable recording medium recorded with a compiler program of claim 6,

wherein said code generating function is constituted to generate a code for determining a leading address of an interface area, based on a designation by a user.

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